



MITS

MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE

(Deemed to be University under section 3 of UGC Act, 1956)

CIRCULAR

Date: 29.05.2026
Madanapalle

All the M. Tech (VLSI & Embedded Systems) I Year II Semester (R25) Regular students are hereby informed that the Practical End Semester Examination will be conducted as per the time table given below.

Time Table

Course Name & Code: CMOS Analog IC Design Laboratory-25MBVESLC03

Date	Branch/ Section	Time	Roll No.S	Venue	No.of Students
12-06-2026 (Friday)	M.Tech (VES)	10.00 AM to 01.00 PM	25MRB02001 - 25MRB02022	LB-103	22

Course Name & Code: Embedded System Design Laboratory-25MBVESLC04

Date	Branch/ Section	Time	Roll No.S	Venue	No.of Students
15-06-2026 (Monday)	M.Tech (VES)	10.00 AM to 01.00 PM	25MRB02001 - 25MRB02022	LB-103	22

Course Name & Code: QNX Based RTOS-25MBVESSC02

Date	Branch/ Section	Time	Roll No.S	Venue	No.of Students
17-06-2026 (Wednesday)	M.Tech (VES)	10.00 AM to 01.00 PM	25MRB02001 - 25MRB02022	LB-103	22

LB-Lakshmi Block

Controller of Examinations

Copy to : The Website

The File

Notice Board

ECE- HoD

CONTROLLER OF EXAMINATIONS
Madanapalle Institute of Technology & Science
(Deemed to be University)
MADANAPALLE - 517 325, A. P.